REVISION HISTORY

REV	DESCRIPTION	DATE	APRVL	MANAGER
0	ENGINEERING PROTOTYPE	10/06/92	GRR	
0A	DEVELOPER/BUGFIX RELEASE	01/03/03	GRR	
1	ADVANCE ENGINEEERING RELEASE	2/3/93	GRR	
2	Revised per ECO 930095	3/17/93		D.Sommer
A	PILOT PRODUCTION RELEASE	4/15/93		
B	REVISED PER ECO 930162	5-7-93		
C	REVISED PER ECO 930177 (REV3)	6-1-93		
D	REVISED PER ECO 930222	6-17-93	B.BARTLE	
E	REVISED PER ECO930238	7/20/93		

SIGNAL GLOSSARY

SIGNAL	DESCRIPTION (AREA)	PAGES
28MHZ	28.63636 MHZ MASTER CLOCK	
7MHZ	7.15909 MHZ PROCESSOR CLOCK	
A[23:1]	PROCESSOR ADDRESS BUS (68000)	
ACK	DATA ACKNOWLEDGE (PARALLEL PORT)	
AS	ADDRESS STROBE (68000)	
AUDIN	AUDIO INPUT (RS232 PORT)	
AUDOUT	AUDIO OUTPUT (RS232 JACK)	
BEER	BUS ERROR (68000)	
BG	BUS GRANT (68000)	
BGACK	BUS GRANT ACKNOWLEDGE (68000)	
BLISS	BLITTER SLOWDOWN (CHIPS)	
BLIT	CHIP MEMORY ACCESS (CHIPS)	
BR	BUS REQUEST (68000)	
BUSY	DEVICE BUSY (PARALLEL PORT)	
CASL/U	COLUMN ADDRESS STROBE (DRAM)	
CCK/CCKQ	COLOR CLOCK / QUADRATURE (CHIPS)	
CDAC	7.15909 MHZ QUADRATURE CLOCK (CHIPS)	
CHNG	MEDIA CHANGE (FLOPPY)	
CLKRD/WR	READ-TIME CLOCK READ / WRITE (RTC)	
COMP	MONOCHROME COMPOSITE VIDEO (VIDEO)	
CSYNC	COMPOSITE SYNC (VIDEO)	
CTS	CLEAR TO SEND (RS232 PORT)	
D[15:0]	PROCESSOR DATA BUS (68000)	
DIR	STEP DIRECTION (FLOPPY)	
DKRD	DISK READ DATA (FLOPPY)	
DKWD	DISK WRITE DATA (FLOPPY)	
DKWE	DISK WRITE ENABLE (FLOPPY)	
DMAL	CHIP DMA REQUEST LINE (CHIPS)	
DRA[8:0]	DRAM ADDRESS BUS (DRAM)	
DRD[15:0]	DRAM DATA BUS (DRAM)	
DSR	DATA SET READY (RS232 PORT)	
DTACK	DATA TRANSFER ACKNOWLEDGE (68000)	
DTR	DATA TERMINAL READY (RS232 PORT)	
E	PERIPHERAL ENABLE CLOCK (68000)	
EXTICK	EXPANSION PRESENT / RTC TICK	
FC[2:0]	FUNCTION CODE (68000)	
FIRE0/1	FIRE BUTTON 0/1 (JOYSTICKS)	
HLT	PROCESSOR HALT (68000)	
HSYNC	HORIZONTAL SYNC (VIDEO)	
INDEX	INDEX PULSE (FLOPPY)	
INT[2.3.6]	INTERRUPT REQUEST (CHIPS)	
IORESET	I/O RESET	
IPL[2:0]	INTERRUPT PRIORITY LEVEL (68000)	
KBCLOCK	KEYBOARD CLOCK (KEYBOARD)	
KBDATA	KEYBOARD DATA (KEYBOARD)	
KBRESET	KEYBOARD RESET (KEYBOARD)	
LDS/UDS	UPPER / LOWER DATA STROBES (68000)	
LED	POWER ON LED / AUDIO FILTER DISABLE	
LEFT/RIGHT	LEFT RIGHT AUDIO (AUDIO)	

SIGNAL	DESCRIPTION (AREA)	PAGES
LPEN	LIGHT PEN TRIGGER (JOYSTICKS)	
MTR	MOTOR ON (FLOPPY)	
MTR0	MOTOR ON - DRIVE 0 (FLOPPY)	
MOV/MOH	MOUSE 0 QUADRATURE V/H (JOYSTICKS)	
M1V/M1H	MOUSE 1 QUADRATURE V/H (JOYSTICKS)	
OVL	OVERLAY ROM OVER RAM	
OVR	OVERRIDE SYSTEM DECODING	
PIXELSW	GENLOCK PIXEL SWITCH (VIDEO)	
POTOX/OY	POT LINES 0 X/Y (JOYSTICKS)	
POT1X/1Y	POT LINES 1 X/Y (JOYSTICKS)	
POUT	PAPER OUT (PARALLEL PORT)	
PPD[7:0]	PHRALLEL PORT DATA (PARALLEL PORT)	
RAMEN	RAM ENABLE (CHIPS)	
REGEN	CHIP REGISTER ENABLE (CHIPS)	
RAS0/1	ROW ADDRESS STROBE (DRAM)	
RDY	DRIVE READY (FLOPPY)	
RESET	GENERAL RESET	
RGA[8:1]	REGISTER ADDRESS BUS (CHIPS)	
R/G/B	RED / GREEN / BLUE (VIDEO)	
RI	RING INDICATE (RS232 PORT)	
ROMEN	ROM ENABLE (ROM)	
RTS	REQUEST TO SEND (RS232 PORT)	
RST	PROCESSOR RESET (68000)	
RXD	RECEIVE DATA (RS232 PORT)	
RW	PROCESSOR READ/WRITE (68000)	
SEL	SELECT (PARALLEL PORT)	
SEL[3:0]	DRIVE SELECT (FLOPPY)	
SIDE	SIDE SELECT (FLOPPY)	
STEP	STEP IN/OUT COMMAND (FLOPPY)	
TRK0	TRACK ZERO SENSE (FLOPPY)	
TXD	TRANSMIT DATA (RS232 PORT)	
VMA	VALID MEMORY ADDRESS (68000)	
VPA	VALID PERIPHERAL ADDRESS (68000)	
VSYNC	VERTICAL SYNC (VIDEO)	
WE	WRITE ENABLE (DRAM)	
WPROT	WRITE PROTECT SENSE (FLOPPY)	
XCLK	EXTERNAL GENLOCK CLOCK (VIDEO)	
XCLKEN	EXTERNAL CLOCK ENABLE (VIDEO)	
XRDY	EXTERNAL DATA READY	
	** CREDIT CARD AND IDE STUFF? **	

KEY COMPONENTS

REF	CHIP	DESCRIPTION	PAGE
U1	68000	68000 PROCESSOR 16MHZ	2
U2	8374	ALICE (AA AGNUS)	2
U3	8364	PAULA	5
U4	4203	LISA (AA DENISE)	4
U5	F0022X	AA AKIKO (CBM ASIC)	2,8,11
U6	ASST	ROM 512KX16, 150 NS	10
U7-8	8520	AMIGA VIA, 1 MHZ	7
U10-11	28F10	FLASH MEMORY 128KX8	10
U12	CXA1145	SONY VIDEO ENCODER	4
U13	6570-36	AMGIA KEYBOARD MPU	9
U14	NE555	RESET TIMER	9
U15	LF347	BIMOS OP-AMP	5
	TL084	BICMOS OP-AMP	ALT
U16-17	ASST	DRAM 256KX16, 100 NS	3
U18-19	ASST	DRAM 256KX16 OPTIONAL	3
U20	391???	BRIDGETTE (NCR ASIC)	2
U28	1488	EIA LINE DRIVER	7
U29	1489	EIA LINE RECEIVER	7
U30	BT101	TRIPLE 8-BIT VIDEO DAC	4
X1	OSC	TTL 28.63636 MHZ NTSC	2
	OSC	TTL 28.37512 MHZ PAL	ALT
Y451	XTAL	4.43619MHZ PAL BURST	4
Y621	XTAL	3MHZ CERAMIC RESONATOR	9
X2	ASST	PAL VIDEO MODULATOR	4
	ASST	NTSC VIDEO MODULATOR	4

REV 3 PCB

DIRECTORIES AND REVISION HISTORY

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DESIGN BY	D. SOMMER	DATE	10/06/92
DRAWN BY		DATE	
CHECK BY		DATE	
CHECK			
APPR			

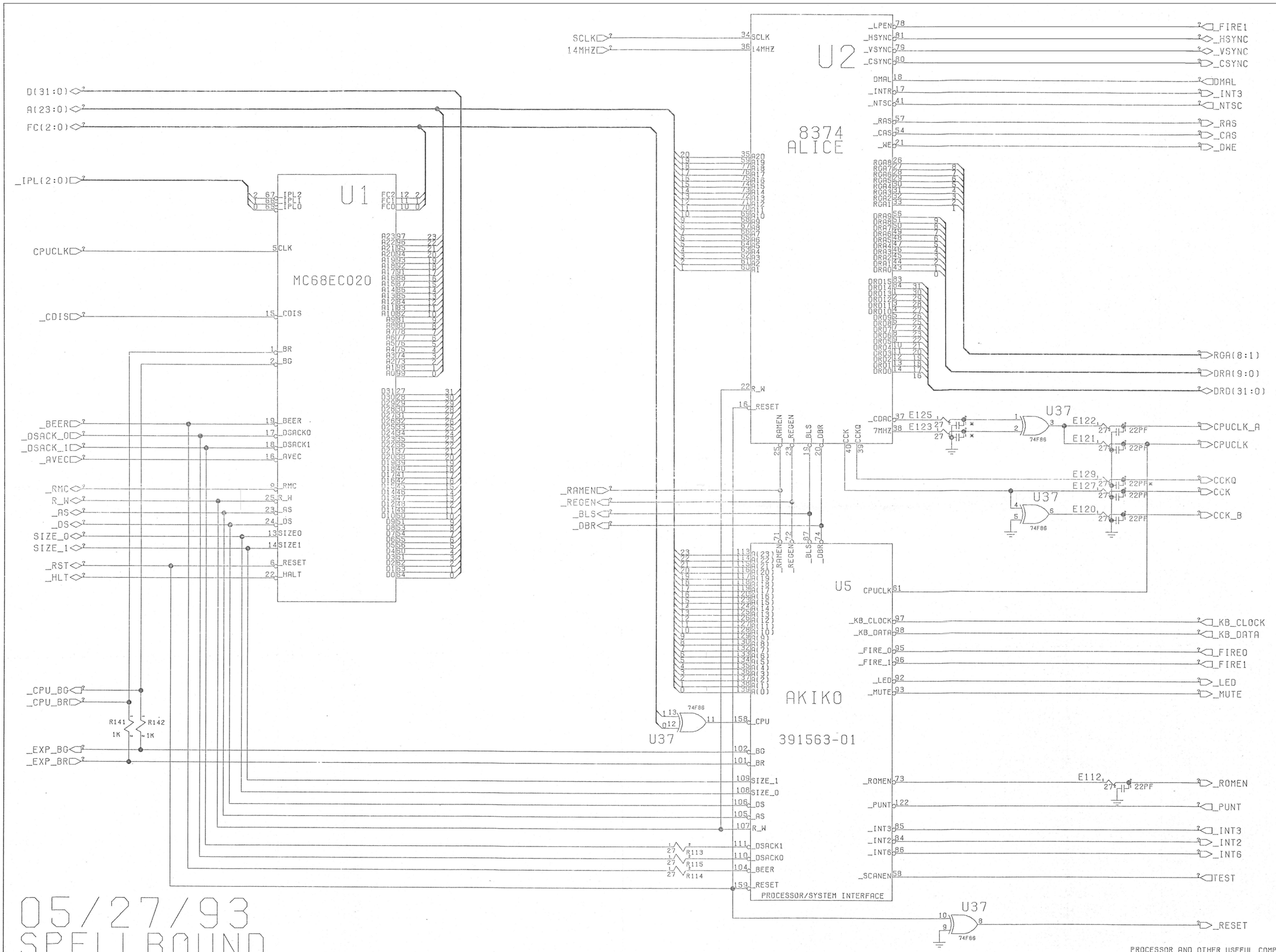
COMMODORE
SCHEMATIC
CD/GAME CONSOLE BASE UNIT - "SPELLBOUND"

USED ON	NEXT ASSY	SIZE
C/A1200	365101	C

365100 REV E
SCALE
SHEET 1 OF 20

PATH: I:\PROJECTS\SPELLBOUND\REVISN SHEET1

vds_output



05/27/93
SPELLBOUND
CD/GS PLAYER PCB

NOTE: VARIOUS COMPONENTS ARE FOR EMI CONTROL
AND MAY BE LOADED WITH FUNNY THINGS...

PROCESSOR AND OTHER USEFUL COMPONENTS

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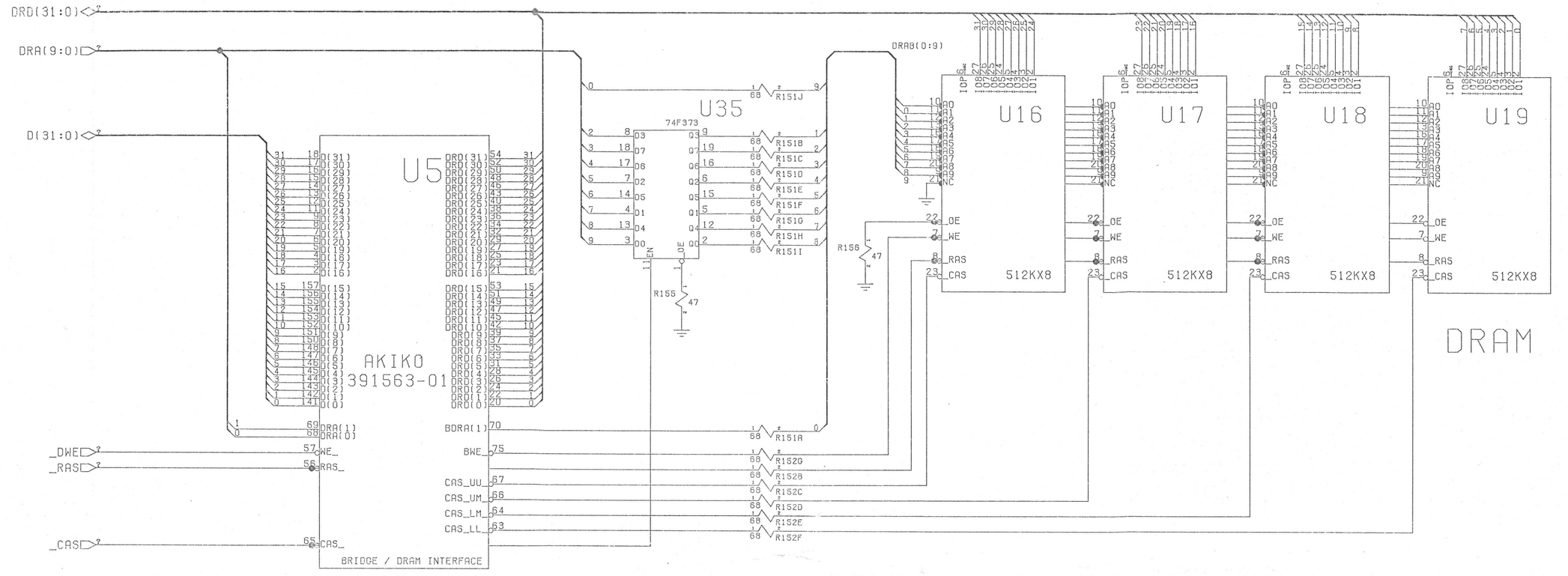
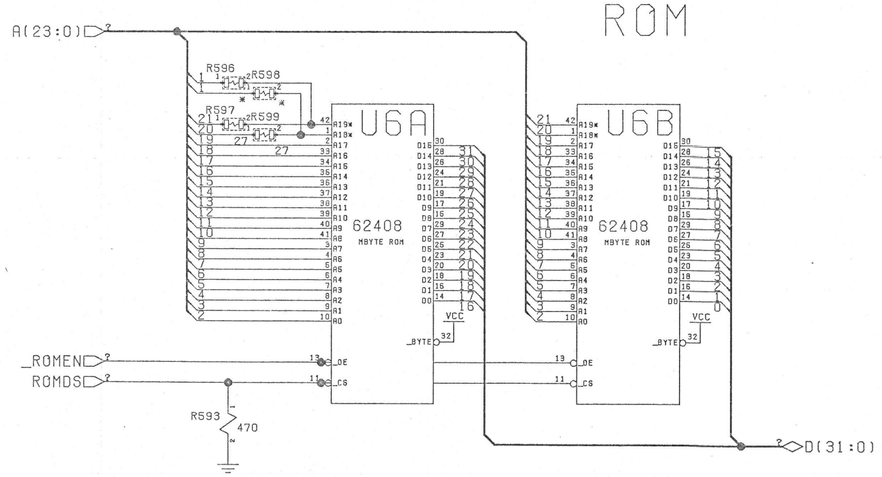
DESIGN BY	D. ROBBINS 04/05/93
DRAWN	ORK 08/06/92
CHK'D	DAF 05/15/93
APP'D	
USED ON	C/A1000
NEXT ASSY	365101

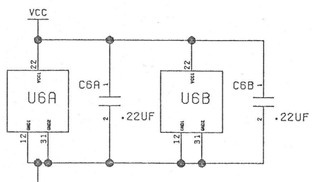
COMMODORE
SCHEMATIC CD/GS REV 3
CD/GAME SYSTEM PLAYER UNIT - "SPELLBOUND"
SIZE C | 365100 | E
SCALE
SHEET 2 OF 10

PATH: /PROJECT/A500/SPELLBOUND.REV3 SHEET2

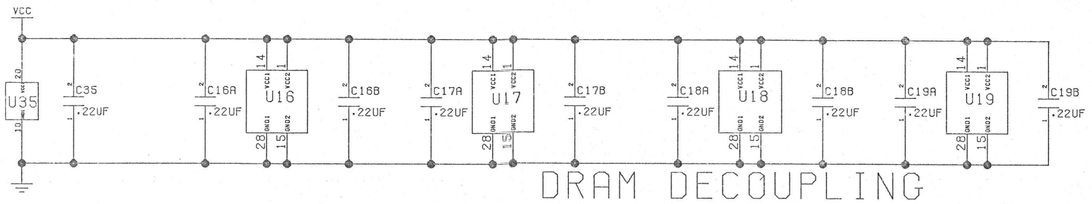
vds_output



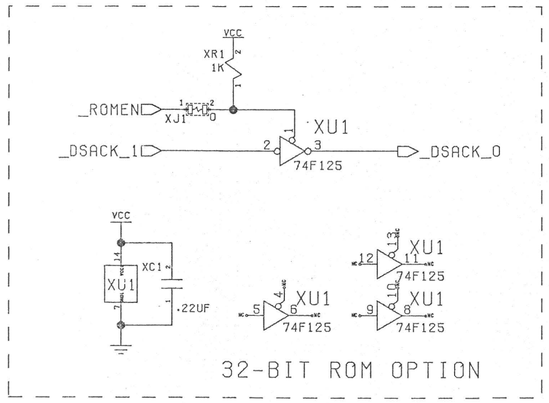




ROM DECOUPLING



DRAM DECOUPLING



32-BIT ROM OPTION

CD/GS PLAYER PCB

vds_output

MEMORY AND...WELL, I USED TO REMEMBER

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DESIGN BY: D. ROBBINS	DATE: 08/05/92	COMMODORE
DRAWN: ORR	09/04/92	
LAST+ EDIT	05/10/93	SCHEMATIC CD/GS REV 3
CHKD+		CD/GAME SYSTEM PLAYER
APPR+		UNIT - "SPELLBOUND"
USED ON: C/A100	NEXT ASSY: 365101	SIZE: C 365100 REV: E
		SCALE: SHEET 3 OF 10

PATH= /PROJECT/A500/SPELLBOUND.REV2 SHEET3

VIDEO DAC

CLOCK MUX

DECOUPLING

CD/GS PLAYER PCB

G/I-PAL ONLY

M/N-PAL ONLY

CXA1145 ENCODER

MODULATOR

S-VIDEO

COLOR

U4 LISA 4203

U30 BT101 VIDEO DAC

U12 CXA1145 ENCODER

RGA(8:1)

DRD(31:0)

_PIXELSW

_BLANK

DR

DG

DB

DI

AR

AG

AB

_EXT_VIDEO

_PIXELSW_EXT

AR_EXT

AG_EXT

AB_EXT

_CAS

_RST

CCK

MDAT

_MLD

SCLK

14MHZ

PIXELCLK

XCLK

_XCLKEN

28MHZ

NOTE: PAL USES 28.37516 MHZ

CCK_BC

_CSYNC

RIGHT

LEFT

_NTSC

DENISE IS PRETTY MUCH INTO VIDEO...

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DESIGN BY	D. ROBBINS	DATE 08/05/92
DRAWN	GKB	08/05/92
LAST+	BRF	07/15/93
CHKD+		
APPR+		
USED ON	NEXT ASST	
C/A100	365101	

COMMODORE
SCHEMATIC CD/GS REV 3
CD/GAME SYSTEM PLAYER
UNIT - "SPELLBOUND"
SIZE C | 365100 | E
SCALE
SHEET 4 OF 10
PATH: /PROJECT/A500/SPELLBOUND.REVZ SHEET4

vds_output

DECOUPLING

VCC
U34
C34
.047UF

MOUSE/JOYSTICK PORTS

LEFT JOY
CN1
DB9P
_FIRE0
POT0Y
POT0X
R352 4.7
E351 E353 E354
470PF
C352 .22UF
6800PF
E355 E356 E357 E358

RIGHT JOY
CN2
DB9P
_FIRE1
POT1Y
POT1X
R362 4.7
E361 E363 E364
470PF
C362 .22UF
6800PF
E365 E366 E367 E368

R370 4.7K

U34
74LS166
SI
A B C D E F G H
QH
S_L
CLK
CK_I
CLR
R379 470
R371 47
E374 68 470PF
_MX
_MDAT
_MLD
CCK_B
_RESET

U3
PAULA
8364
POY 36
POX 35
P1Y 39
P1X 38
C312 C311 C314 C313 .047UF
AUDIO

_IPL(2:0)
IPL2
IPL1
IPL0

DRD(31:0)
RGA(8:1)
DMAL
CCK
CCKQ
_RST
RESET

RIGHT 33
IRIGHT
C331 6800PF
R331 330
U15A
MC34182 /TL062
R330 330
AVREF
A_RIGHT

LEFT 34
ILEFT
C321 6800PF
R321 330
U15A
MC34182 /TL062
R320 330
AVREF
A_LEFT

R319 1K
_DKRD 40
_DKWD 41
DKWE 42
_RXD 44
_TXD 43
_INT6 21
_INT3 20
_INT2 19

I->V CONVERTER

CN13

_KB_CLOCK
_KB_DATA
R511A 1K
R511B 1K
6-PIN MINI-DIN
E641 27
E642 27
100PF
FB641
E643 68
E644 68
22PF
C641 .22UF
R644 1K
R643 1K
D641 D642 1N4001
D644 D643 1N4001
_RXD
_TXD

KEYBOARD

U14

TPO
_BUTTON
R629 10K
1NS14
D629
R628 10K
C629 .47UF
RH5VA43A

RESET

CD/GS PLAYER PCB

vds_output

PAULA PREFERS THE TRADITIONAL MODES

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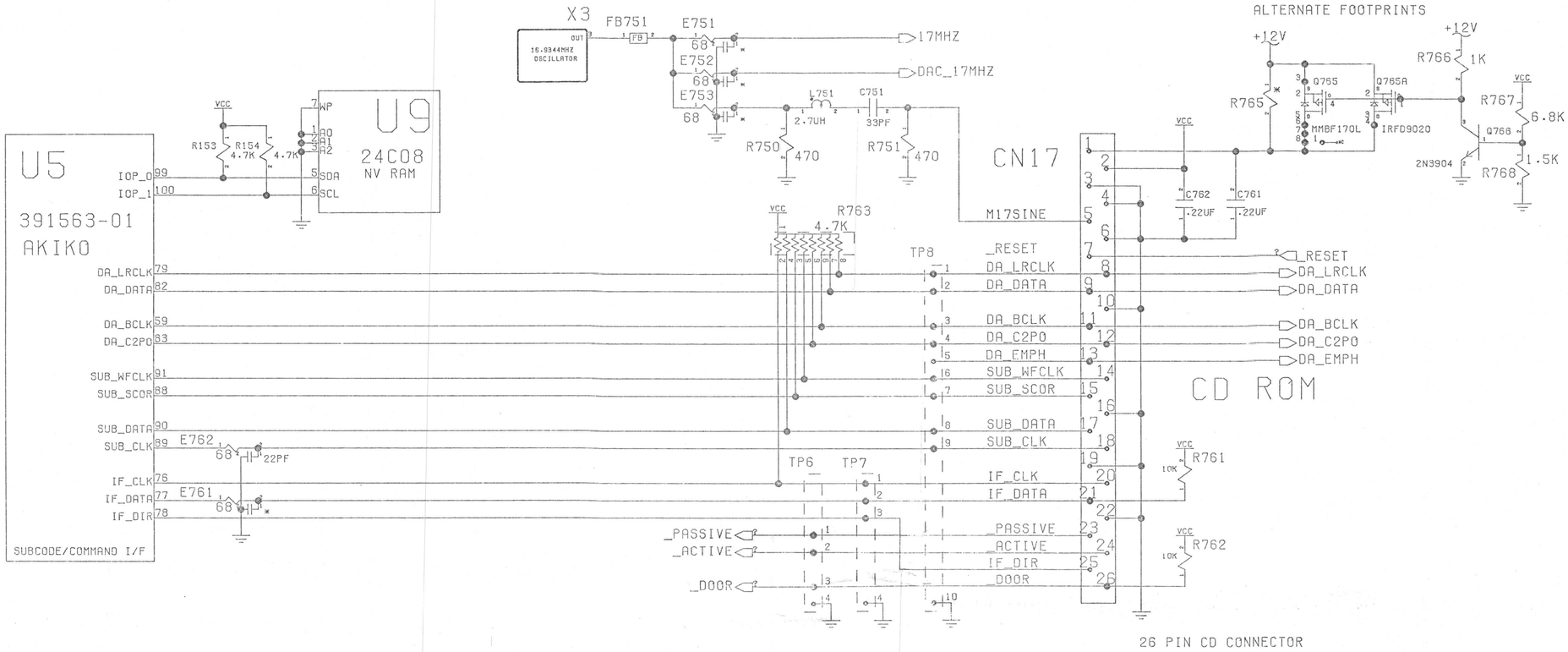
DESIGN BY: D. ROBBINS	DATE: 08/05/92	COMMODORE
DRAW: DEB	09/05/92	
LAST: DAF	11/16/93	SCHEMATIC CD/GS REV 3
CHKD:		CD/GAME SYSTEM PLAYER
APPR:		UNIT - "SPELLBOUND"
USED ON: C/A100	NEXT ASSY: 365101	SIZE C 365100 REV E
		SCALE SHEET 5 OF 10

PATH: /PROJECT/A500/SPELLBOUND.KEY2 SHEET5



CD/GS PLAYER PCB

CD ROM, THE WAVE OF THE FUTURE - NOT!

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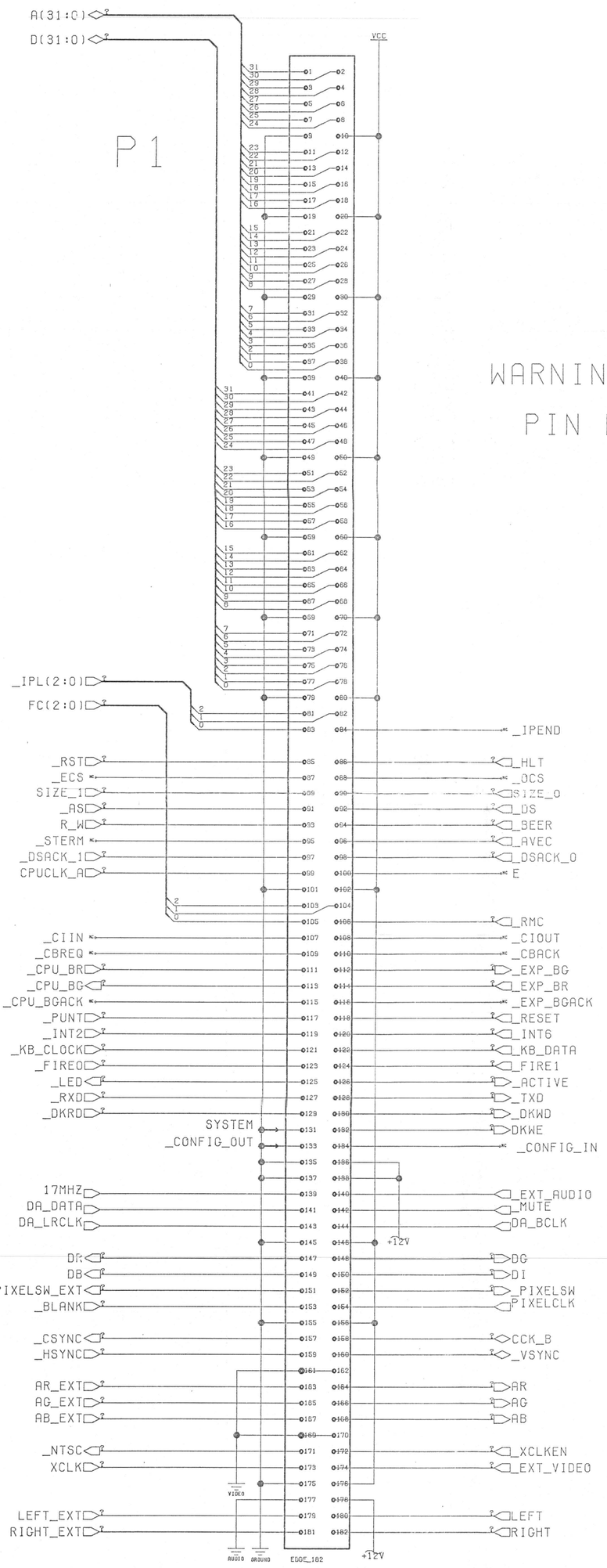
DESIGN BY: S. ROBBINS	DATE: 08/08/92	COMMODORE
DRN: GRB	08/08/92	SCHEMATIC CD/GS REV 3
LAST: RHF	09/15/92	CD/GAME SYSTEM PLAYER
CHK:		UNIT - "SPELLBOUND"
APP:		
USED ON: C/A100	NEXT ASSY: 365101	SIZE C | 365100 | REV E
		SCALE | SHEET 6 OF 10

PATH: /PROJECT/A500/SPELLBOUND.REV2 SHEET6

vds_output

EXPANSION CONNECTOR



WARNING:

PIN NUMBERS ARE BACKWARDS

CD/GS PLAYER PCB

EXPANSION OR IS IT MANIFEST DESTINY?

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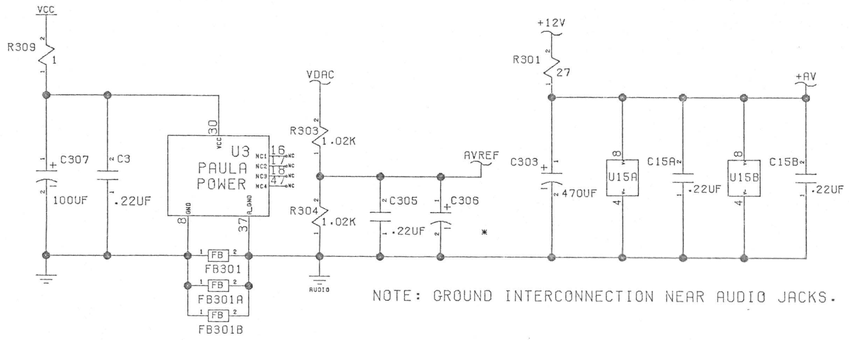
DESIGN BY: C. ROBBINS	DATE: 06/06/92	COMMODORE
DRAWN: GPR	06/06/92	SCHEMATIC CD/GS REV 3
CHKD:	07/16/92	CD/GAME SYSTEM PLAYER
APPR:		UNIT - "SPELLBOUND"
USED ON: C/A1CO	NEXT ASSY: 365101	SIZE C 365100 REV E
		SCALE
		SHEET 5 OF 10

PATH= /PROJECT/A1CO/SPELLBOUND.REV1A SHEET 5

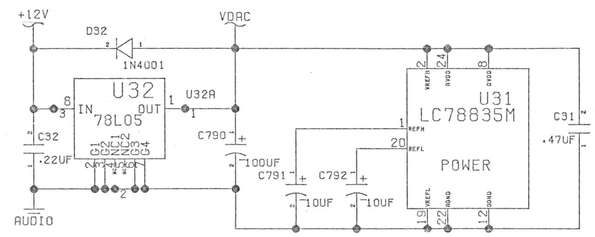
vds_output

PAULA/AUDIO DECOUPLING

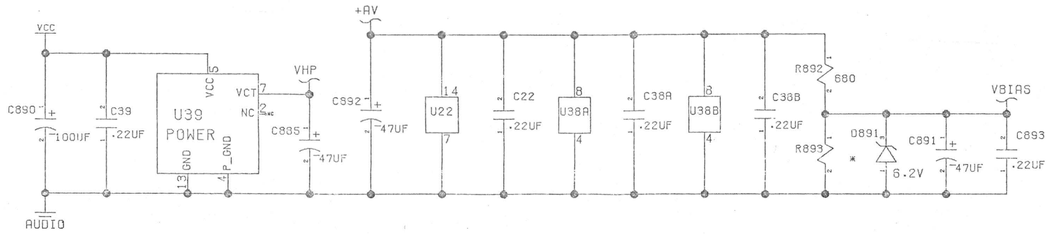


NOTE: GROUND INTERCONNECTION NEAR AUDIO JACKS.

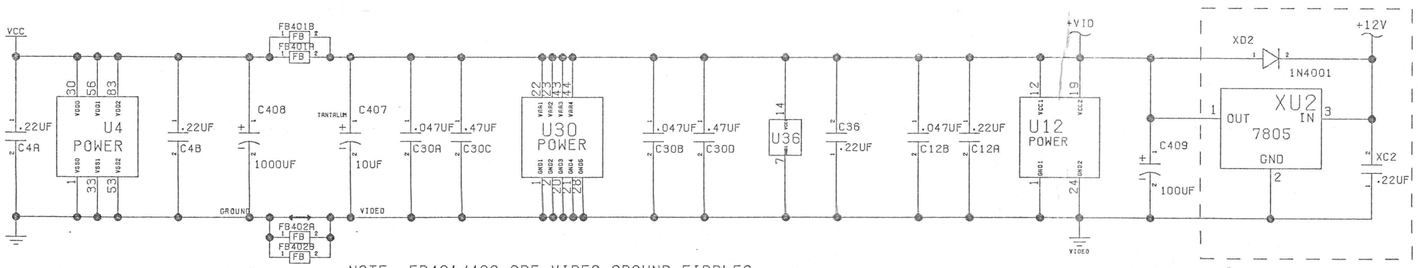
CD/AUDIO DAC DECOUPLING



HEADPHONE/AUDIO DECOUPLING

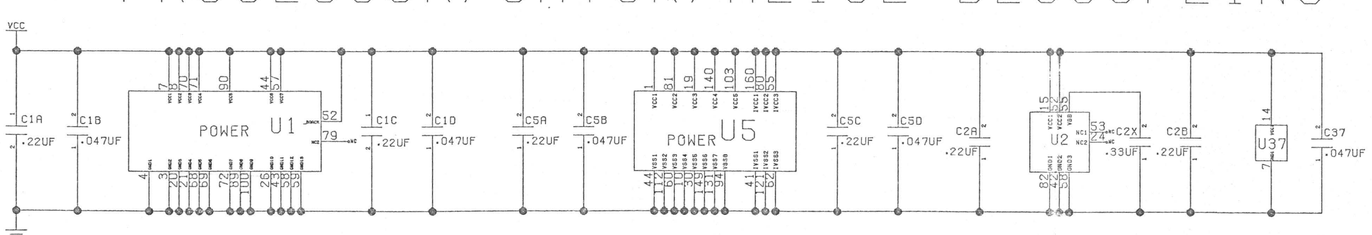


VIDEO DECOUPLING



NOTE: FB401/402 ARE VIDEO GROUND FIDDLES

PROCESSOR/GATOR/ALICE DECOUPLING



CD/GS PLAYER PCB

POWER MANAGEMENT & DECOPULATION DETAIL

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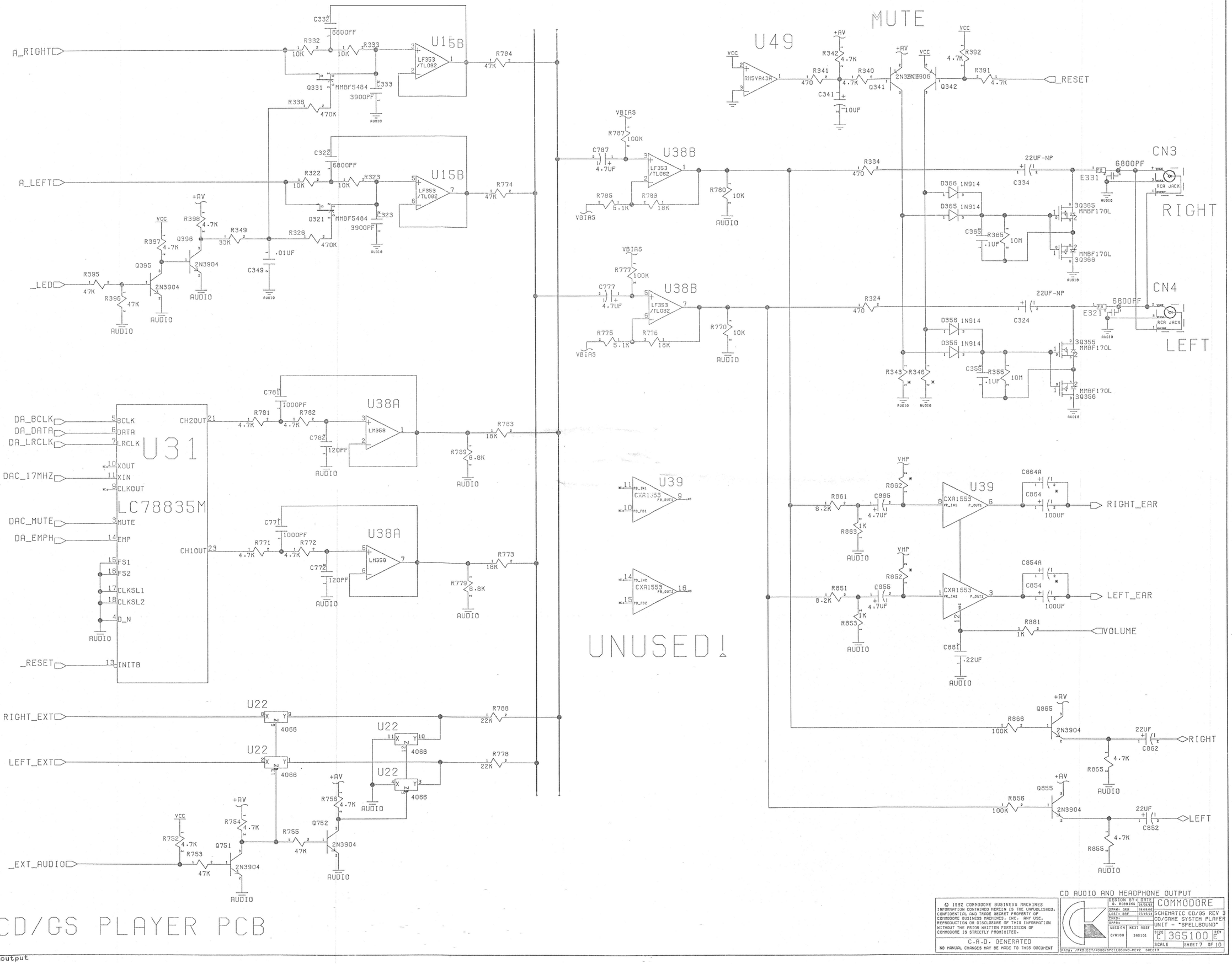
DESIGN BY	DATE
V. ROBBINS	08/06/92
DRAWN GRX	08/06/92
LAST# DAY	09/15/92
CHK'D#	
APP'D#	
USED ON	NEXT ASSY
C/A100	365101

COMMODORE
SCHEMATIC CD/GS REV 3
CD/GAME SYSTEM PLAYER UNIT - "SPELLBOUND"
SIZE C | 365100 | REV E
SCALE | SHEET 10 OF 10

PATH= /PROJECT/A100/SPELLBOUND.REV2 SHEET9


A_RIGHT
A_LEFT
_LED
C332 6800PF
C322 6800PF
R332 10K
R333 10K
R322 10K
R323 10K
U15B
LF353 /TL082
R784 47K
R774 47K
Q331 MMBF5484
Q321 MMBF5484
C333 3900PF
C323 3900PF
R336 470K
R326 470K
C349 .01UF
+AV
R398 4.7K
R349 33K
VCC
R397 4.7K
Q396
Q395 2N3904
2N3904
R395 47K
R396 47K
AUDIO
VBIAS
R787 100K
C787 4.7UF
U38B
R785 5.1K
R786 18K
R760 10K
R777 100K
C777 4.7UF
R775 5.1K
R776 18K
R770 10K
MUTE
U49
RH5VA43A
R342 4.7K
R341 470
R340 4.7K
2N3906
Q341
Q342
R392 4.7K
R391 4.7K
C341 10UF
_RESET
R334 470
R324 470
22UF-NP
C334
C324
D366 1N914
D365 1N914
D356 1N914
D355 1N914
C365 .1UF
R365 10M
C355 .1UF
R355 10M
3Q365 MMBF170L
3Q366 MMBF170L
3Q355 MMBF170L
3Q356 MMBF170L
R343 R346
E331
E321
6800PF
CN3
CN4
RCA JACK
RIGHT
LEFT
DA_BCLK
DA_DATA
DA_LRCLK
DAC_17MHZ
DAC_MUTE
DA_EMPH
_RESET
U31
LC78835M
BCLK
DATA
LRCLK
XOUT
XIN
CLKOUT
MUTE
EMP
FS1
FS2
CLKSL1
CLKSL2
D_N
CINITB
CH2OUT
CH1OUT
C781 1000PF
C782 120PF
C771 1000PF
C772 120PF
R781 4.7K
R782 4.7K
R771 4.7K
R772 4.7K
U38A
LM358
R783 18K
R773 18K
R789 6.8K
R779 6.8K
U39
CXA1553
R862
R861 8.2K
C865 4.7UF
R863 1K
C864A
C864
100UF
RIGHT_EAR
R852
R851 8.2K
C855 4.7UF
R853 1K
C854A
C854
LEFT_EAR
R881 1K
C881 .22UF
VOLUME
UNUSED!
U22
4066
RIGHT_EXT
LEFT_EXT
R788 22K
R778 22K
_EXT_AUDIO
R752 4.7K
R753 47K
R754 4.7K
R755 47K
R756 4.7K
Q751 2N3904
Q752 2N3904
Q865
Q855
R866 100K
R856 100K
C862 22UF
C852 22UF
R865 4.7K
R855 4.7K
RIGHT
LEFT
CD/GS PLAYER PCB
CD AUDIO AND HEADPHONE OUTPUT
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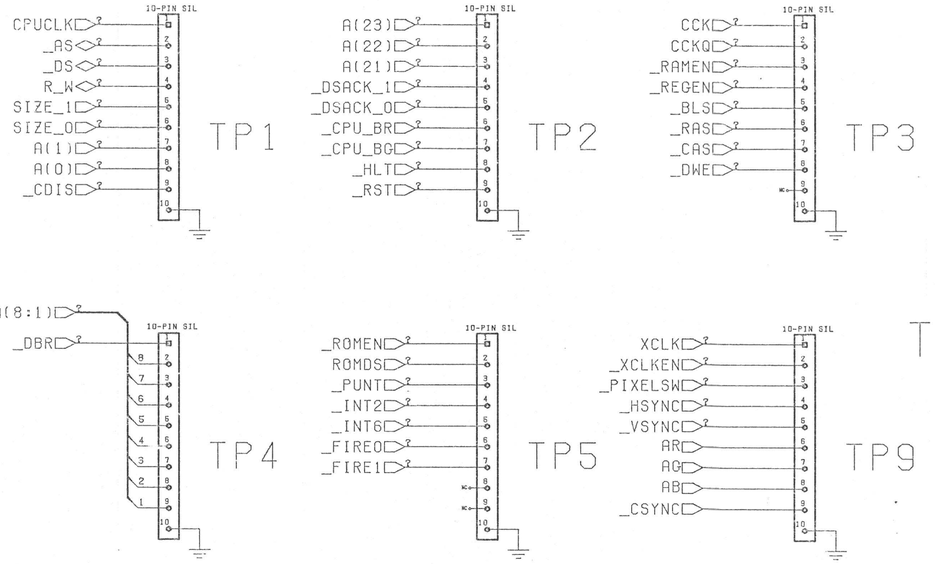
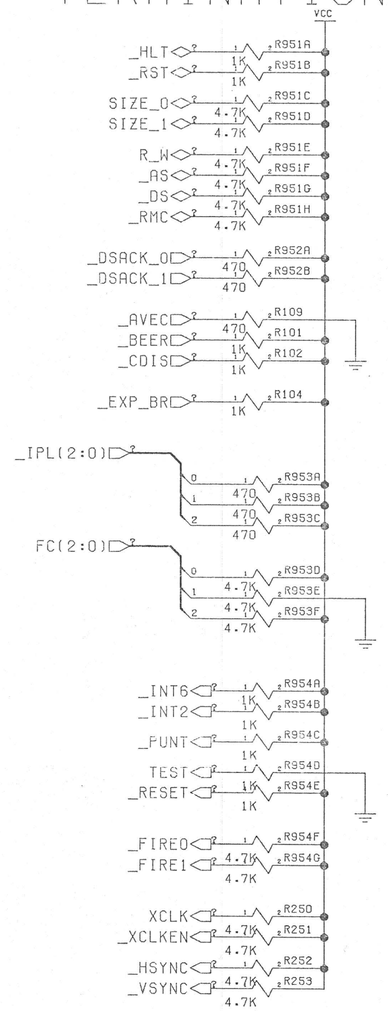
DESIGN BY: S. ROBBINS
DATE 08/06/92
DRAWN: GRR 08/06/92
CHK'D: BRF 05/16/93
APPR'D:
USED ON C/A100
NEXT ASSY 365101
COMMODORE
SCHEMATIC CD/GS REV 3
CD/GAME SYSTEM PLAYER UNIT - "SPELLBOUND"
SIZE C 365100 REV E
SCALE
SHEET 7 OF 10
PATH: /PROJECT/A500/SPELLBOUND.REV2 SHEET7
vds_output

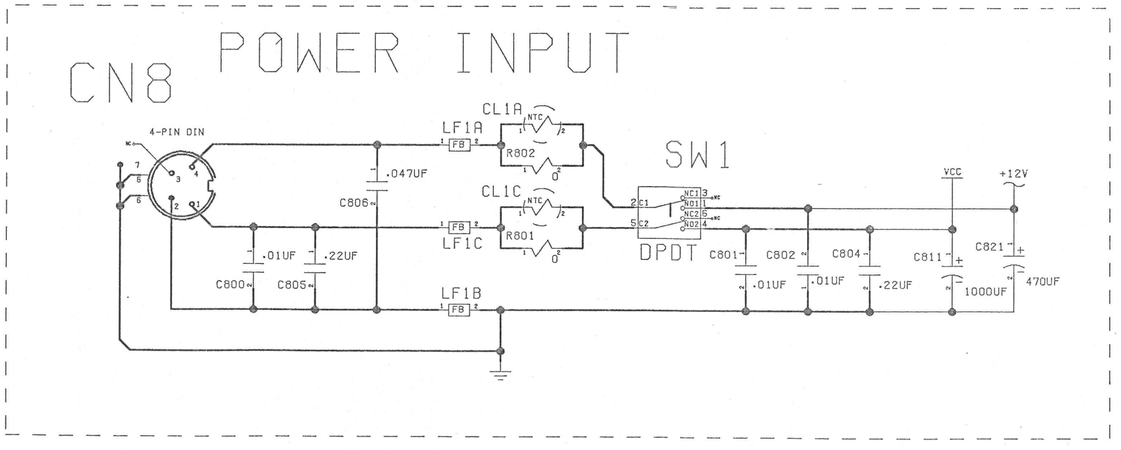
TEST ACCESS



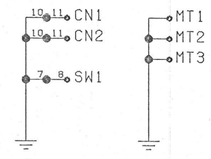
TERMINATION



POWER INPUT



HOLES &C.



CD/GS PLAYER PCB

USER INTERFACE AND RELATED DEAD ENDS

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DESIGN BY/I	DATE	COMMODORE
D. ROBBINS	06/05/92	
DRAWN BY	08/05/92	
LAST EDIT BY	07/15/93	SCHEMATIC CD/GS REV 3
CHKD BY		CD/GAME SYSTEM PLAYER
APPR BY		UNIT - "SPELLBOUND"
USED ON	NEXT ASSY	
C/A100	365101	SIZE C 365100 REV C
		SCALE
		SHEET 9 OF 10

PATH: /PROJECT/A300/SPELLBOUND.REV2 SHEET9

vds_output